



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CYPR-CD01209M

6/10/03  
9.12.03  
Aweel

Inventor(s): Craig Nemecek

Serial No.: 09/998,859

Group Art Unit: 2825

Filed: 11/15/01

Examiner: Thompson, A.M.

Title: A SYSTEM AND A METHOD FOR CHECKING LOCK STEP CONSISTENCY BETWEEN AN IN  
CIRCUIT EMULATION AND A MICROCONTROLLER WHILE DEBUGGING PROCESS IS IN  
PROGRESS

Commissioner of Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following document:

Author	Title	Date	Place of Publication
Cypress Microsystems	CYPRESS MICROSYSTEMS UNVEILS PROGRAMMABLE SYSTEM-ON-A-CHIP FOR EMBEDDED INTERNET, COMMUNICATIONS AND CONSUMER SYSTEMS,	11/13/00	<a href="http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html">http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html</a>

Please direct all correspondence concerning the above-identified application to the following address:

**WAGNER, MURABITO & HAO LLP**  
Two North Market Street, Third Floor  
San Jose, California 95113  
(408) 938-9060

Respectfully submitted,

08/29/2003 AOSMAN1 00000002 09998859

01 FC:1806

Date:

180.00 OP  
8/25/03

By:

Ronald M. Pomerence  
Reg. No. 43,009